JAN 1 1 2006 SAV 10/789,038

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Examiner: Ly D. Pham

Serial No.:

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Group Art Unit: 2827

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Title:

INTEGRATED CIRCUIT MEMORY DEVICE AND METHOD

AMENDMENT UNDER 37 C.F.R. § 1.312

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed October 7, 2005, please amend as follows: